



Attorney Docket No.: 2102397-992010

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicants: Sohrab Kianian et al.

Group Art Unit: 2826

Serial No.: 09/982,413

Examiner: Scott R. Wilson

Filed: October 17, 2001

Gray Cary Ware & Freidenrich LLP
2000 University Avenue
East Palo Alto, CA 94303-2248

Title: SEMICONDUCTOR MEMORY ARRAY OF
FLOATING GATE MEMORY CELLS WITH
BURIED BIT-LINE AND VERTICAL WORD
LINE TRANSISTOR

Customer No. 26379

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Attn.: Official Draftsperson
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Dear Sir:

Applicant hereby submits eleven (11) sheets of formal drawings in the above-identified patent application.

Please charge any fees for the submission of the formal drawings to Gray Cary Ware & Freidenrich LLP, deposit account 07-1896. A duplicate copy of this transmittal is enclosed if charge of additional fees is necessary.

GRAY CARY WARE & FREIDENRICH LLP

Dated April 8, 2004

By *Alan A. Limbach*
Alan A. Limbach
Reg. No. 39,749
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TRANSMITTAL FORM (to be used for all correspondence after initial filing)	Application Number	09/982,413	
	Filing Date	October 17, 2001	
	First Named Inventor	Kianian et al.	
	Art Unit	2826	
	Examiner Name	Scott R. Wilson	
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SIGNATURE OF APPLICANT, ATTORNEY, OR AGENT	
Firm Or Individual name	Alan A. Limbach, Reg. No. 39,749 Gray Cary Ware & Freidenrich LLP
Signature	
Date	April 8, 2004

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